

**FORM PTO-1449 (MODIFIED)****LIST OF PUBLICATIONS FOR  
APPLICANT'S INFORMATION  
DISCLOSURE STATEMENT**

Applicant(s): P.G. D'Arcy et al.  
 Case: 13-1  
 Serial No.: 10/028,453  
 Filing Date: December 24, 2001  
 Group: 2121

**U.S. PATENT DOCUMENTS**

EXAMINER	INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE	IF APPROPRIATE
		5,559,837	9/24/96	Blaker et al.			
		5,537,445	7/16/96	Blaker et al.			
		5,533,065	7/2/96	Blaker et al.			
		5,490,178	2/6/96	Blaker et al.			

**RECEIVED**

FEB 03 2003

Technology Center 2100

**FOREIGN PATENT DOCUMENTS**

EXAMINER	INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION	YES	NO

**OTHER DOCUMENTS**

EXAMINER	INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

1. A. Weinberger, "4:2 Carry Save Adder Module," IBM Technical Disclosure Bulletin, Vol. 23, pp. 3811-3814, January 1981.
2. G. Fettweis et al., "High-Rate Viterbi Processor: A Systolic Array Solution," IEEE Journal of Selected Areas in Communication, Vol. 8, pp. 1520-1534, October 1990.

Examiner

Date Considered

7/29/2004

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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